

## **S P E C I F I C A T I O N**

Docket No. **SILA0003**

TO ALL WHOM IT MAY CONCERN:

BE IT KNOWN THAT WE, **Philip C.W. Yip, Paul M. Brown, Jr., and Brian S. Mirkin**, all citizens of the United States of America, residing in the State of Texas, have invented new and useful improvements in a

### **METHOD FOR TRAINING A RECEIVING MODEM**

of which the following is a specification:

## BACKGROUND OF THE INVENTION

## 1. Technical Field

The present invention relates to modems in general, and in particular to a method for synchronizing modems. Still more particularly, the present invention relates to an improved method for training a receiving modem.

## 2. Description of the Related Art

It is well-known in the art that modems can be utilized to perform data communications between data processing systems. Depending on a specific application, the data processing system can be a personal computer, a network server, a point-of-sale credit card reader, etc. Modems are typically capable of transmitting as well as receiving data. For ease of identification, a modem is denoted as a sending modem when transmitting data, and the modem is denoted as a receiving modem when receiving data.

Typically, a receiving modem employs timing recovery techniques to recover a symbol rate at which data are transmitted. A receiving modem also uses synchronization techniques to align the clock of the receiving modem with the clock of a sending modem. Synchronization signals are often transmitted near the beginning of a "handshaking" protocol during which a training procedure is performed within the receiving modem. Synchronization signals are also transmitted periodically during data transmission in order to maintain synchronization between the receiving modem and the sending modem.

During a handshaking protocol, a training procedure is performed by transmitting multiple training frames from the sending modem to the receiving modem. The training procedure is important because it ensures the receiving modem can compensate for the impairments on a telephone line before data transfer should begin. Thus, the

1 training procedure needs to be long enough to ensure the receiving modem is able to  
2 recover all the transmitted data. However, if the training procedure is too long, it becomes  
3 the bottle-neck of a data transaction session, especially when the data transaction session  
4 is relatively short. For example, in point-of-sale applications, the data that need to be  
5 transmitted for a credit card transaction is usually much shorter than the data that needed  
6 to be transmitted for the training procedure. Consequently, it would be desirable to provide  
7 an improved method for training receiving modems such that optimal performances can be  
8 achieved.

## SUMMARY OF THE INVENTION

In accordance with a preferred embodiment of the present invention, a sending modem trains a receiving modem by performing a four-segment training procedure. During segment 1 training, the sending modem waits for silence on a transmission line between the sending modem and the receiving modem for 48 symbol intervals. Then, the sending modem performs segment 2 training by sending alternating AB symbols to the receiving modem for 64 symbol intervals. During segment 3 training, the sending modem sends CD symbols to the receiving modem for 64 symbol intervals in order to train an equalizer within the receiving modem. Specifically, the coefficients of the equalizer within the receiving modem are being modified in accordance with the received CD symbols from the sending modem during segment 3 training. During segment 4 training, the sending modem continues to train the equalizer within the receiving modem by sending scrambled binary "1" symbols to the receiving modem for 48 symbol intervals. Also during segment 4 training, a subset of the 48 estimated symbols from the equalizer within the receiving modem are descrambled for the purpose of accuracy verification. After a successful completion of the segment 4 training, the receiving modem can change to a data mode to begin detecting and receiving data from the sending modem.

All objects, features, and advantages of the present invention will become apparent in the following detailed written description.

## BRIEF DESCRIPTION OF THE DRAWINGS

The invention itself, as well as a preferred mode of use, further objects, and advantages thereof, will best be understood by reference to the following detailed description of an illustrative embodiment when read in conjunction with the accompanying drawings, wherein:

Figure 1 is a block diagram of a data communication environment using pulse code modulation (PCM) based modems in which a preferred embodiment of the present invention is applicable;

Figure 2 is a detailed block diagram of a modem, in accordance with a preferred embodiment of the present invention; and

Figure 3 is a high-level logic flow diagram of a method for training a receiving modem, in accordance with a preferred embodiment of the present invention.

## DETAILED DESCRIPTION OF A PREFERRED EMBODIMENT

Referring now to the drawings and in particular to Figure 1, there is depicted a block diagram of a data communication environment using pulse code modulation (PCM) based modems in which a preferred embodiment of the present invention is applicable. As shown, a data communication environment 10 includes a data processing device 11 coupled to a modem 12 and a data processing device 18 coupled to a modem 17. Data processing devices 11 and 12 can be a personal computer, a network server, a point-of-sale device or any type of data processing device that is well-known in the art. Data processing device 11 is connected to a telephone network 15 through a modem 12 having a transmitter 13 and a receiver 14. The connection between transmitter 13 and telephone network 15 is a digital connection having a data rate of approximately 64 kbps. Telephone network 15 is connected to a local loop 19 through a central office line card 16. Since the parameters of telephone network 15 and line card 16 are dictated by the specifications of a public telephone company, transmitter 13 needs to transmit digital data in a particular format to fully exploit its digital connection to telephone network 15. Those skilled in the art will appreciate that any known signal processing, coding, and decoding techniques related to, for example,  $\mu$ -law signal point constellations, shell mapping, spectral control, equalizer training, and the like, can be utilized within central office line card 16. Local loop 19 is connected to data processing device 18 via modem 17. Similar to modem 12, modem 17 includes a transmitter and a receiver.

Modem 12 may format digital data for transmissions to modem 17, where the digital data is retrieved for use by data processing device 18. The data may be arranged into data symbols and encoded via any number of well-known techniques. The data symbols may then be further processed with spectral control (or other signal conditioning schemes) prior to being transmitted as one or more signal segments at a particular symbol rate to telephone network 15. Modem 17 eventually receives the data symbols and thereafter decodes the data to retrieve the original digital data. To function efficiently,

1 modem 17 should be synchronized with modem 12. Modem 17 may include a timing  
2 recovery scheme that recovers the transmitted symbol rate and synchronizes the receiver  
3 at modem 17 with transmitter 13 within modem 12.

4

5 Both modem 12 and modem 17 can be either a sending modem or a  
6 receiving modem, depending on a specific application. Since modem 12 and modem 17  
7 are functionally similar to each other, only modem 17 is further described in details. With  
8 reference now Figure 2, there is depicted a detailed block diagram of modem 17, in  
9 accordance with a preferred embodiment of the present invention. As shown, modem 17  
10 includes a system-side line-isolation module 21 and a line-side line-isolation module 31.  
11 System-side line-isolation module 21 and line-side line-isolation module 31 are coupled to  
12 each other via an isolation barrier 30. Isolation barrier 30 is preferably a capacitor.  
13 System-side line-isolation module 21 includes a microcontroller 22, an isolation interface  
14 23, a multiplexer 24, an universal asynchronous receiver transmitter (UART) 25, a control  
15 interface 26, a digital signal processor (DSP) 27, a clock interface 28, and an audio  
16 coder-decoder (CODEC) 29.

17

18 System-side line-isolation module 21 includes a transmit pin TXD, a receive  
19 pin RXD, a reset pin RESET\*, a clear-to-send pin CTS\*, a clock output pin CLKOUT,  
20 crystal oscillator pins XTALI and XTALO, and an analog output pin AOUT. System-side  
21 line-isolation module 21 also includes four general purpose programmable input/output pins  
22 GPIO1, GPIO2, GPIO3, and GPIO4. Each of the above-mentioned four general purpose  
23 programmable input/output pins may be set up as analog in, digital in, or digital out pins,  
24 depending on user programming of pin functionality. For example, the GPIO1 pin may  
25 also function as an end of frame pin EOFR. The GPIO2 pin may provide an analog in pin  
26 AIN. The GPIO3 pin may function as an escape pin ESC for controlling command or data  
27 modes. The GPIO4 pin may function as an alert pin ALERT for signaling events such as  
28 an intrusion event.

1                   UART 25 converts parallel bytes from microcontroller 22 into serial bits for  
2                   transmission to and receipt from an external device through the transmit pin TXD and the  
3                   receive pin RXD, respectively. UART 25 may operate in, for example, an 8-bit word  
4                   format or a 9-bit word format for serial data transmission through the transmit pin TXD  
5                   and/or the receive pin RXD.

6  
7                   In addition to signal processing, DSP 27 also functions as a data pump.  
8                   Microcontroller 22 provides AT command decoding and call progress monitoring and may  
9                   employ, for example, a 4-bit program word and an 8-bit data word. Clock interface 28  
10                  includes a clock generator that accepts a high-frequency (*e.g.*, 4.9152 MHz) master clock  
11                  input. Clock interface 28 also generates all the modem sample rates for supporting the  
12                  modem standards designed into system-side line-isolation module 21. In addition, the clock  
13                  generator within clock interface 28 provides a 9.6 kHz rate for audio playback.

14  
15                  Programming and control of system-side line-isolation module 21 is  
16                  accomplished by sending appropriate commands through an serial interface. For example,  
17                  commands may be sent by an external device that load internal registers within system-side  
18                  line-isolation module 21 that control the operation and functionality of system-side  
19                  line-isolation device 21.

20  
21                  Line-side line-isolation module 31 includes an isolation interface 32, a hybrid  
22                  circuit 33 and a ring detect and off-hook circuit 34. Hybrid circuit 33 may include a DC  
23                  termination circuit as well as an analog-to-digital converter (ADC) circuit and a  
24                  digital-to-analog converter (DAC) circuit. Line-side line-isolation module 31 includes a  
25                  receive input pin RX, filter pins FILT and FILT2, a reference pin REF, a DC termination  
26                  pin DCT, voltage regulation pins VREG and VREG2, external resistor pins REXT and  
27                  REXT2, ring pins RNG1 and RNG2 and transistor connection pins QB, QE and QE2.  
28

1                   Filter pins FILT and FILT2 set the time constant for the DC termination  
2                   circuit. The reference pin REF may be connected to an external resistor to provide a high  
3                   accuracy reference current. The DC termination pin DCT provides DC termination for a  
4                   telephone phone line and an input for voltage monitors. Voltage regulation pins VREG and  
5                   VREG2 may be connected to external capacitors to provide a bypass internal power supply.  
6                   External resistor pins REXT and REXT2 provide real and complex AC termination. Ring  
7                   pins RNG1 and RNG2 may be connected through capacitors to "tip" and "ring" to provide  
8                   ring and caller ID signals across isolation barrier 30. Transistor connection pins QB, QE  
9                   and QE2 may be connected to external bipolar hook-switch transistors.

10  
11                   If modem 17 from Figure 1 is to be utilized as a receiving modem, then an  
12                   adaptive equalizer needs to be provided within modem 17. Such adaptive equalizer is  
13                   preferably implemented in software or firmware to be executed by DSP 27 within modem  
14                   17. In addition, a training procedure needs to be performed on the adaptive equalizer  
15                   before modem 17 should receive data from a sending modem, such as modem 12 from  
16                   Figure 1. Basically, the training procedure generates approximately 40 to 60 coefficients  
17                   for the adaptive equalizer in order to ensure that modem 17 can compensate for the  
18                   impairments on a telephone line between modem 17 and modem 12 during data  
19                   transmissions. The coefficients for the adaptive equalizer can be generated by the following  
20                   equation:

21  
22                    $w_n(k+1) = w(k) + 0.1/\delta(r_k) * e(k) * r(k-n)$

23  
24                   *where*     $r_k$        =       input sample  
25                     $\delta(r_k)$     =       input power of input sample  
26                     $w_n$        =       equalizer coefficients  
27                     $e(k)$        =       error between ideal symbol and equalizer output  
28                    n         =       index of equalizer coefficients  
29                    k         =       time index

1                   To begin the training procedure, the sending modem initiates the  
2 transmission of a set of synchronizing signals to the receiving modem. The synchronizing  
3 signals are defined in Table 5/V.29 of the 9600 bits per second modem standardized for use  
4 on point-to-point 4-wire leased telephone-type circuits specification promulgated by the  
5 Telecommunication Standardization Sector of the International Telecommunication Union,  
6 the pertinent of which is incorporated by reference herein.

7                   According to the above-mentioned specification, the synchronizing signals  
8 for all data signalling rates are divided into four segments, namely, segment 1, segment 2,  
9 segment 3 and segment 4. The number of symbol intervals in segment 1, segment 2,  
10 segment 3 and segment 4 are 48, 128, 384 and 48, respectively. Specifically, segment 1  
11 of the synchronizing signals is simply waiting for silence for the duration of 48 symbol  
12 intervals. Segment 2 of the synchronizing signals includes alterations between a signal  
13 element A and a signal element B in the form of ABAB ... ABAB for 128 symbol  
14 intervals.

16                   Segment 3 of the synchronizing signals transmits a signal element C and a  
17 signal element D according to an equalizer conditioning pattern. The equalizer conditioning  
18 pattern is a pseudo-random sequence generated by a polynomial:  $1 + x^6 + x^7$ . Element C  
19 is transmitted when the pseudo-random sequence contains a "0," and element D is  
20 transmitted when the pseudo-random sequence contains a "1." Segment 3 begins with the  
21 sequence CDCDCDC ... according to the pseudo-random sequence and continues for 384  
22 symbol intervals. Segment 4 of the synchronizing signals transmits a set of scrambled  
23 binary "1" symbols for 48 symbol intervals.

25                   The analog signal received over the telephone line is typically demodulated  
26 and detected by digital techniques that involve analog to digital (A/D) conversion before  
27 reaching the adaptive equalizer. The sampling method of the A/D conversion can be used  
28 to assist the receiving modem to correctly detect a received symbol. In particular, the

1 symbol boundaries are determined accurately by the receiving modem, and then the  
2 decision as to which symbol has been received is made at the peak of the symbol power  
3 where the accuracy of the decision is maximal and best modem performance can be  
4 achieved.

5  
6 In accordance with a preferred embodiment of the present invention, only  
7 64 symbol intervals are utilized to adjust the adaptive equalizer during the segment 3  
8 training. In addition, the segment 4 scrambled binary "1" symbols are also utilized to  
9 adjust the adaptive equalizer during segment 4 training. By utilizing the segment 4  
10 scrambled binary "1" symbols to train the adaptive equalizer, the training period for the  
11 adaptive equalizer can be effectively extended by 48 symbols without incurring additional  
12 overhead.

13  
14 During the training procedure, a coarse timing algorithm is performed to  
15 determine the deviation from the ideal point at which the received signal should be  
16 sampled.

17  
18 Referring now to Figure 3, there is depicted a high-level logic flow diagram  
19 of a method for training a receiving modem, in accordance with a preferred embodiment  
20 of the present invention. Starting at blocking 40, the sending modem performs segment 1  
21 training by waiting for silence for 48 symbol intervals, as shown in block 41. Then, the  
22 sending modem performs segment 2 training by sending alternating AB symbols for 64  
23 symbol intervals, as depicted in block 42. Next, the sending modem performs segment 3  
24 training by sending CD symbols for 64 symbol intervals, as shown in block 43, to generate  
25 corresponding coefficients for the adaptive equalizer within the sending modem. The  
26 sending modem then performs segment 4 training by sending scrambled binary "1" symbols  
27 for 48 symbol intervals, as depicted in block 44, to adjust the coefficients of the adaptive  
28 equalizer within the sending modem. A subset of the 48 estimated symbols generated from  
29 the 48 scrambled binary "1" symbols can be descrambled for the purpose of accuracy

1 verification, as shown in block 45. Such accuracy verification can be performed in  
2 concurrence with the training step shown in block 44. For example, the last 8 to 10 of the  
3 48 estimated symbols from the output of the adaptive equalizer can be utilized for verifying  
4 the accuracy of the adaptive equalizer. After a successful completion of the segment 4  
5 training, the receiving modem changes to a data mode to begin detecting and receiving data  
6 from the sending modem, as depicted in block 46.

7  
8 As has been described, the present invention provides an improved method  
9 for training receiving modems. With the present invention, it is feasible to make segment  
10 3 training shorter than what is required in the published standard because of the  
11 information, such as the gain to use, equalizer coefficients, etc., gathered in the previous  
12 sessions. In addition, with the present invention, the training period for an adaptive  
13 equalizer can be extended without incurring additional overhead. As a result, a  
14 considerable time saving is achieved when timing adjustments for a given communication  
15 session are made via the usage of a relatively short training procedure.

16  
17 It is also important to note that although the present invention has been  
18 described in the context of a fully functional modem, those skilled in the art will appreciate  
19 that the mechanisms of the present invention are capable of being distributed as a program  
20 product in a variety of forms, and that the present invention applies equally regardless of  
21 the particular type of signal bearing media utilized to actually carry out the distribution.  
22 Examples of signal bearing media include, without limitation, recordable type media such  
23 as floppy disks or CD ROMs and transmission type media such as analog or digital  
24 communications links.

25  
26 While the invention has been particularly shown and described with reference  
27 to a preferred embodiment, it will be understood by those skilled in the art that various  
28 changes in form and detail may be made therein without departing from the spirit and scope  
29 of the invention.